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generating a signal on a first output terminal of the integrated circuit according to the comparison to control the temperature of the integrated circuit, wherein the signal is asserted when the measured temperature is greater than the first limit value, and wherein

the signal on the first output terminal is deasserted when a control location on the integrated circuit is accessed or when the measured temperature goes below a lower limit value, according to a programmable mode of operation.

- 14. Canceled.
- 15. (Amended) The method as recited in claim 13 wherein the asserted signal is used to inhibit a cooling device to control the temperature of the integrated circuit.
 - 16. Canceled.
 - 17. Canceled.
 - 19. Canceled.
 - 20. (Amended) A method comprising:

measuring a temperature of an integrated circuit with a temperature sensor, the temperature sensor being a circuit within the integrated circuit; comparing the measured temperature to a first limit value stored in the integrated circuit; generating a signal on a first output terminal of the integrated circuit according to the comparison to control the temperature of the integrated circuit; and accessing a control location in the integrated circuit to cause the signal to be deasserted.

21. (Amended) The method as recited in claim 13 wherein the asserted signal causes assertion of an interrupt and wherein a sequence of instructions, responsive to the asserted interrupt, agrivates a cooling device.

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23. (Amended) A method comprising:

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measuring a temperature of an integrated circuit with a temperature sensor, the temperature sensor being a circuit within the integrated circuit; comparing the measured temperature to a first limit value stored in the integrated circuit; and

generating a signal on a first output terminal of the integrated circuit according to the comparison to control the temperature of the integrated circuit;

comparing the measured temperature to a second limit value stored in the integrated circuit; and

asserting a second signal on a second output terminal of the integrated circuit when the measured temperature is above the second limit value, thereby indicating that temperature has exceeded a safe limit.